

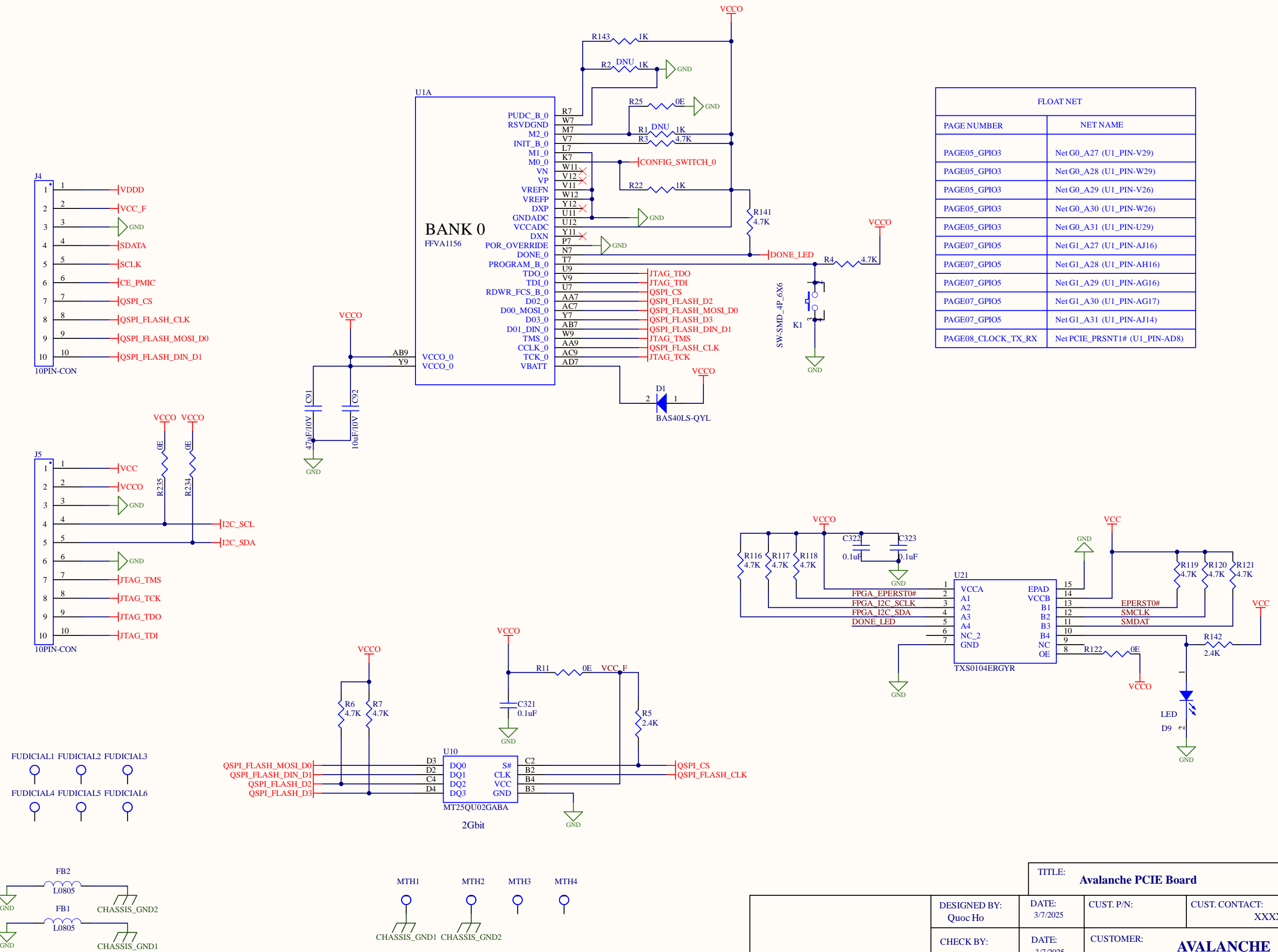
VIN -> 12V -- This is the PCIE 12V supply and powers all the buck DC -D converters. The following below are generated from 12V supply

- VCCINT/VCCINT_IO -> 1.2V
- VCCAUX/VCCAUX_IO/MGTVCCAUX -> 1.8V
- VCC -> 3.0V
- MGTAVTT -> 1.2V
- MGTAVCC -> 0.9V
- VCCBRAM -> 0.85V

CHANGE

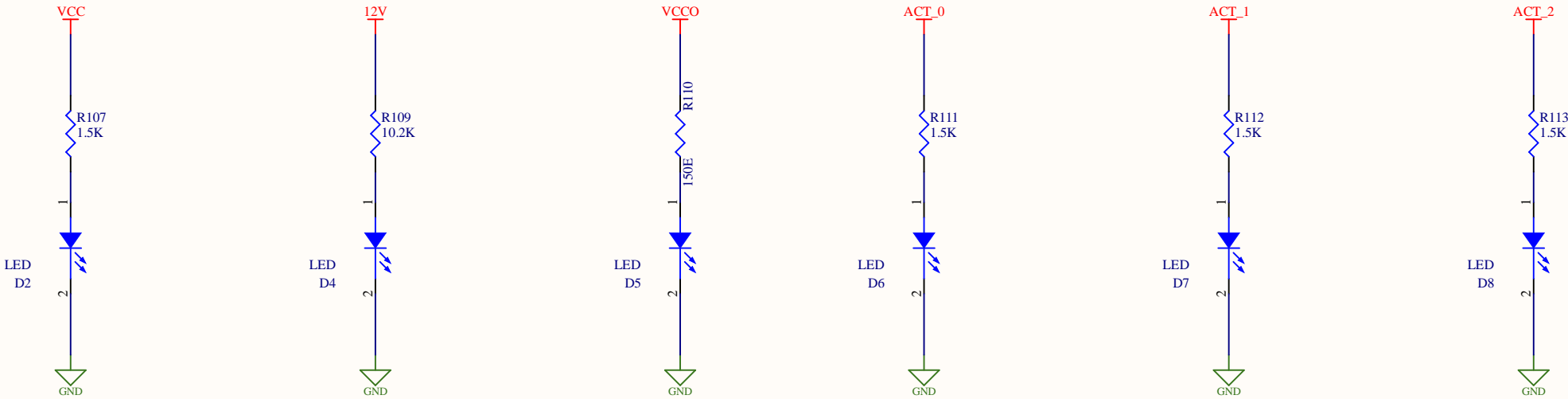
- R54, R234,R235, R144, R145, R146, R147, R148, R149, R150, R151 = DNI
- R55-22K_1%_0402
- R56-10K_1%_0402
- R32-42.2K_1%_0402
- R34-165K_1%_0402
- R19-25.5K_1%_0402
- R35-68K_1%_0402
- C122- 0.01uF
- C126- 0.01uF
- L12- PUT WIRE OR SOLDER ACROSS
- L13- PUT WIRE OR SOLDER ACROSS

				TITLE: Avalanche PCIE Board			
		DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
		CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX		DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 00 OF 28

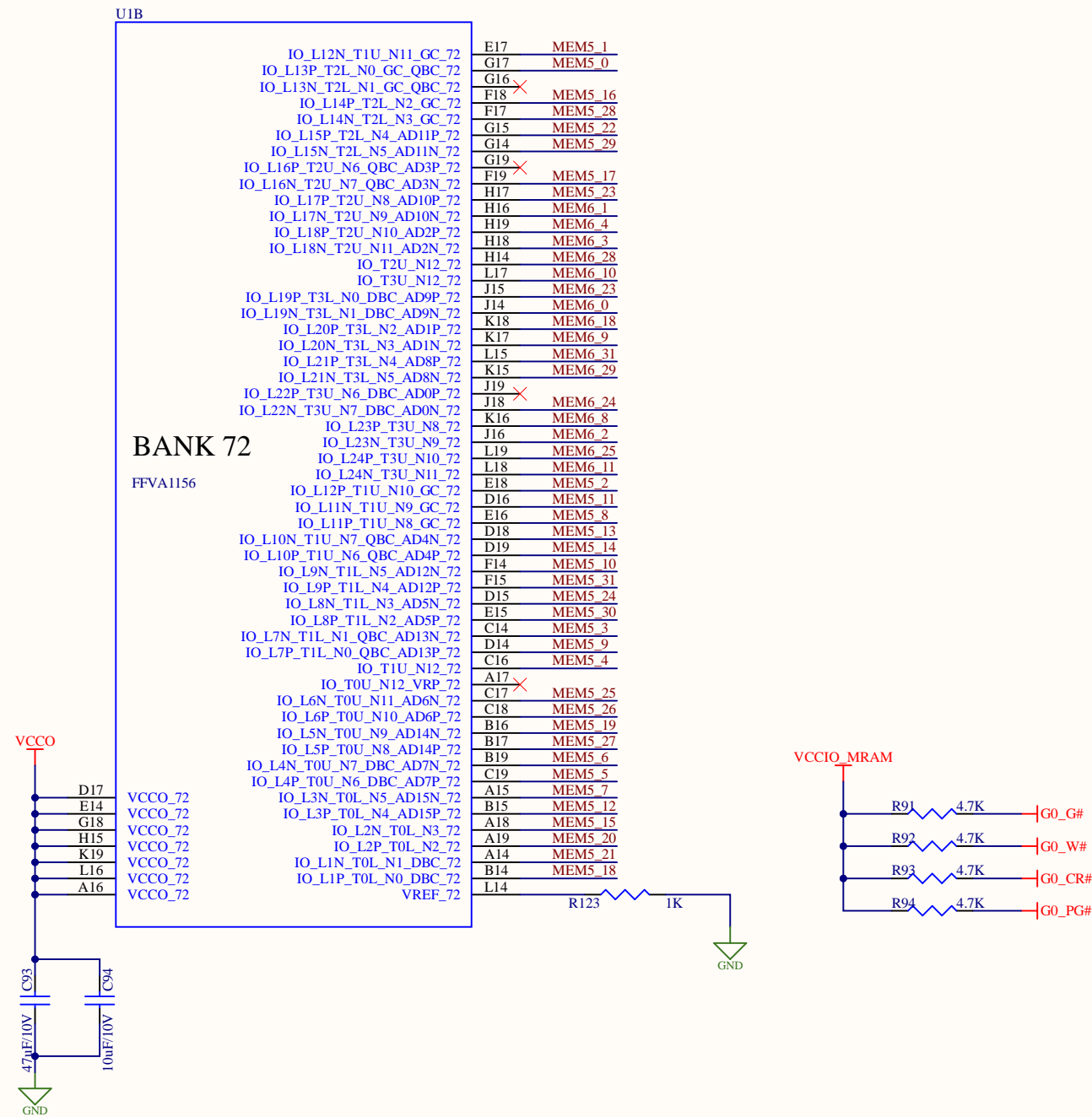


FLOAT NET	
PAGE NUMBER	NET NAME
PAGE05_GPIO3	Net G0_A27 (U1_PIN-V29)
PAGE05_GPIO3	Net G0_A28 (U1_PIN-W29)
PAGE05_GPIO3	Net G0_A29 (U1_PIN-V26)
PAGE05_GPIO3	Net G0_A30 (U1_PIN-W26)
PAGE05_GPIO3	Net G0_A31 (U1_PIN-U29)
PAGE07_GPIO5	Net G1_A27 (U1_PIN-AJ16)
PAGE07_GPIO5	Net G1_A28 (U1_PIN-AH16)
PAGE07_GPIO5	Net G1_A29 (U1_PIN-AG16)
PAGE07_GPIO5	Net G1_A30 (U1_PIN-AG17)
PAGE07_GPIO5	Net G1_A31 (U1_PIN-AJ14)
PAGE08_CLOCK_TX_RX	Net PCIE_PRSNT1# (U1_PIN-AD8)

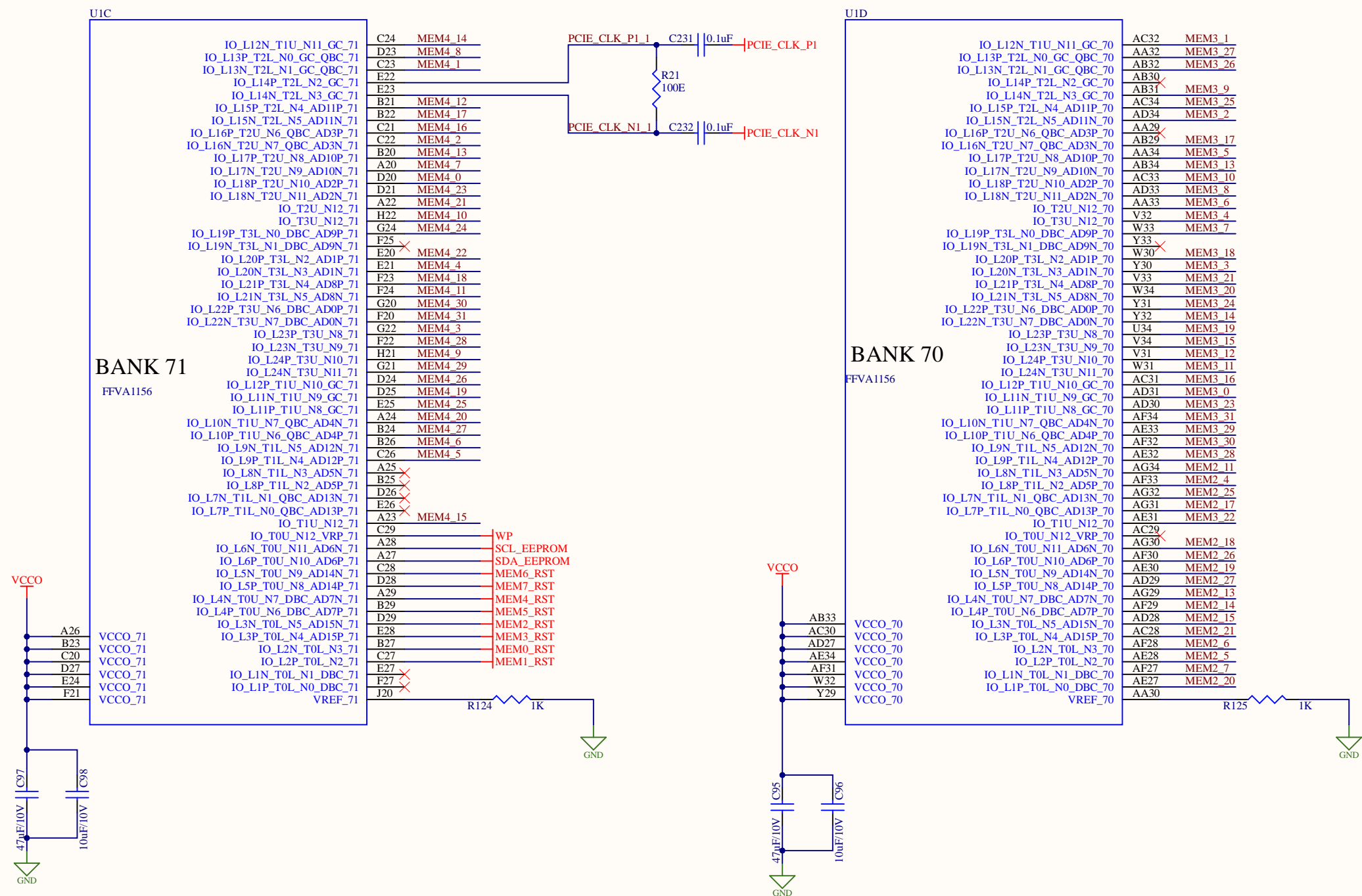
				TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX		
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE				
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 01 OF 28	



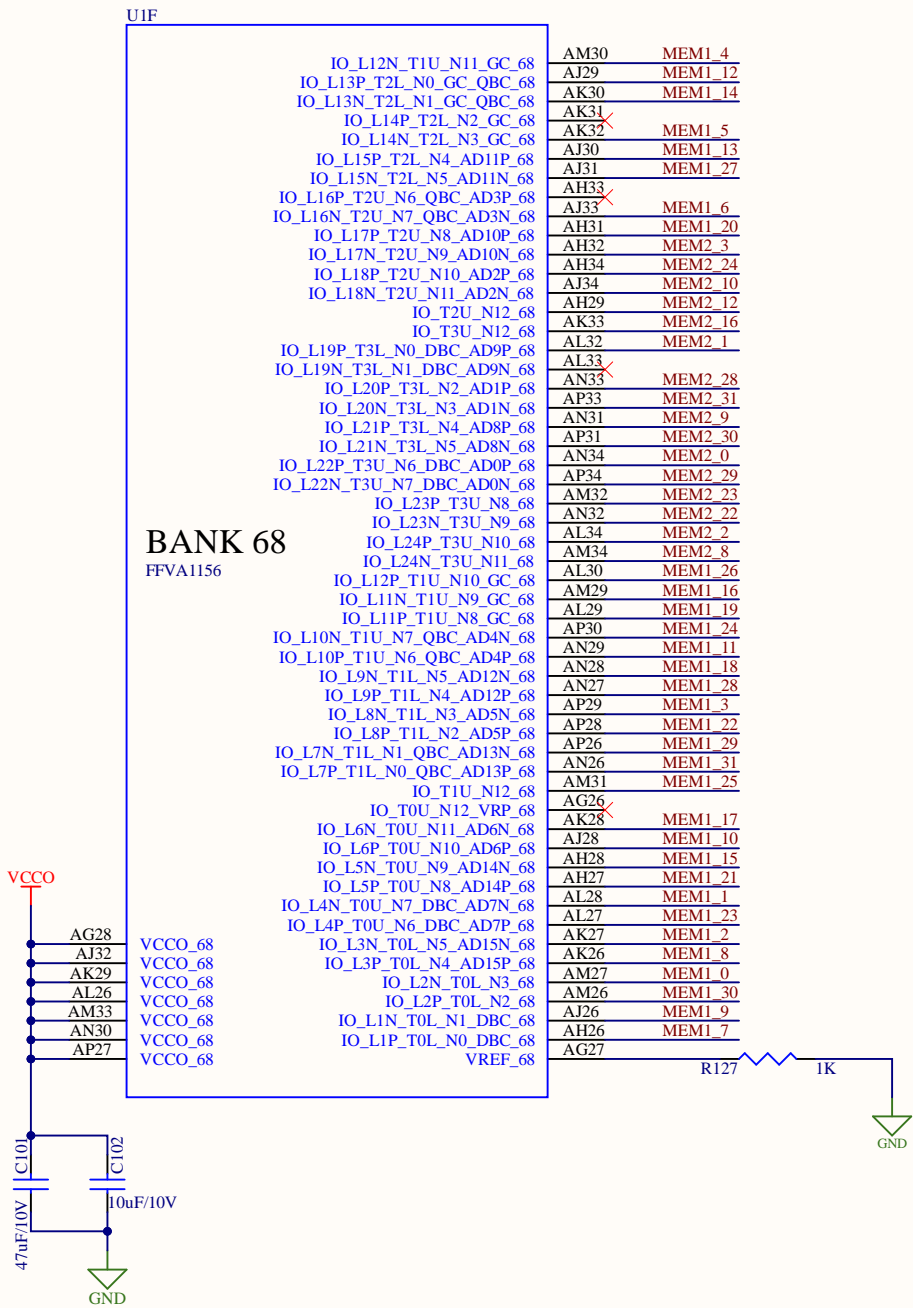
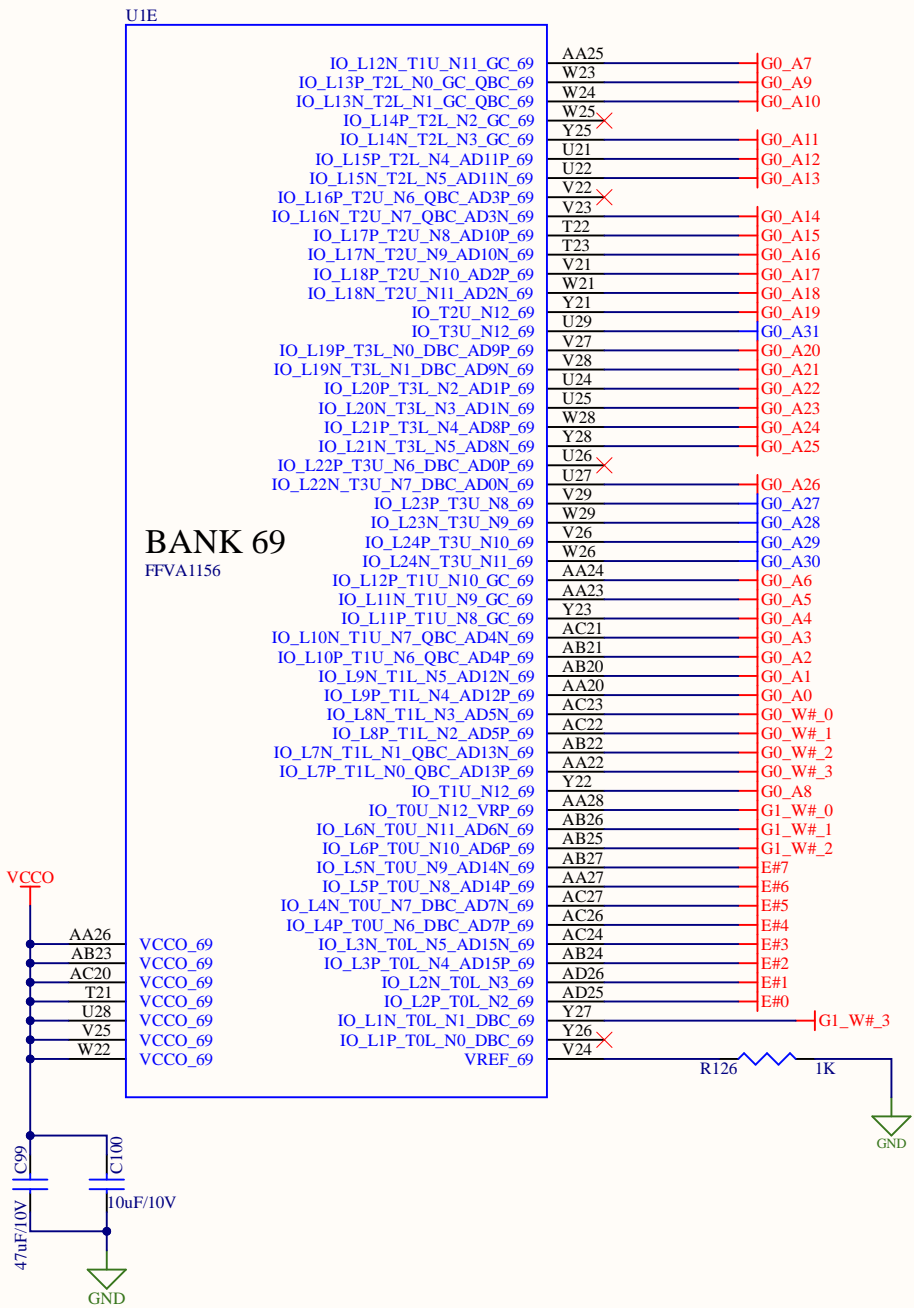
			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:	CUST. CONTACT: XXXX		
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 02 OF 28



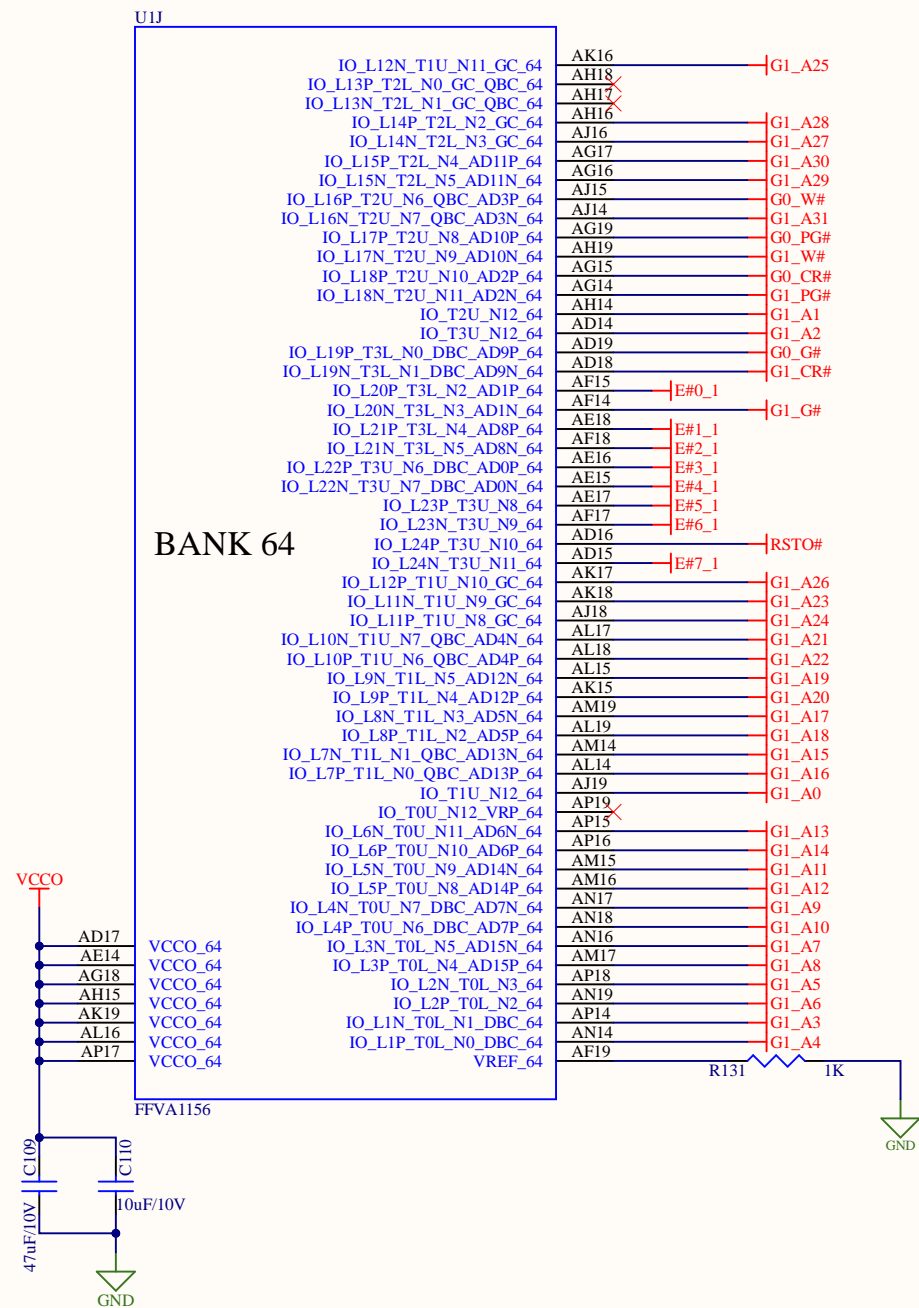
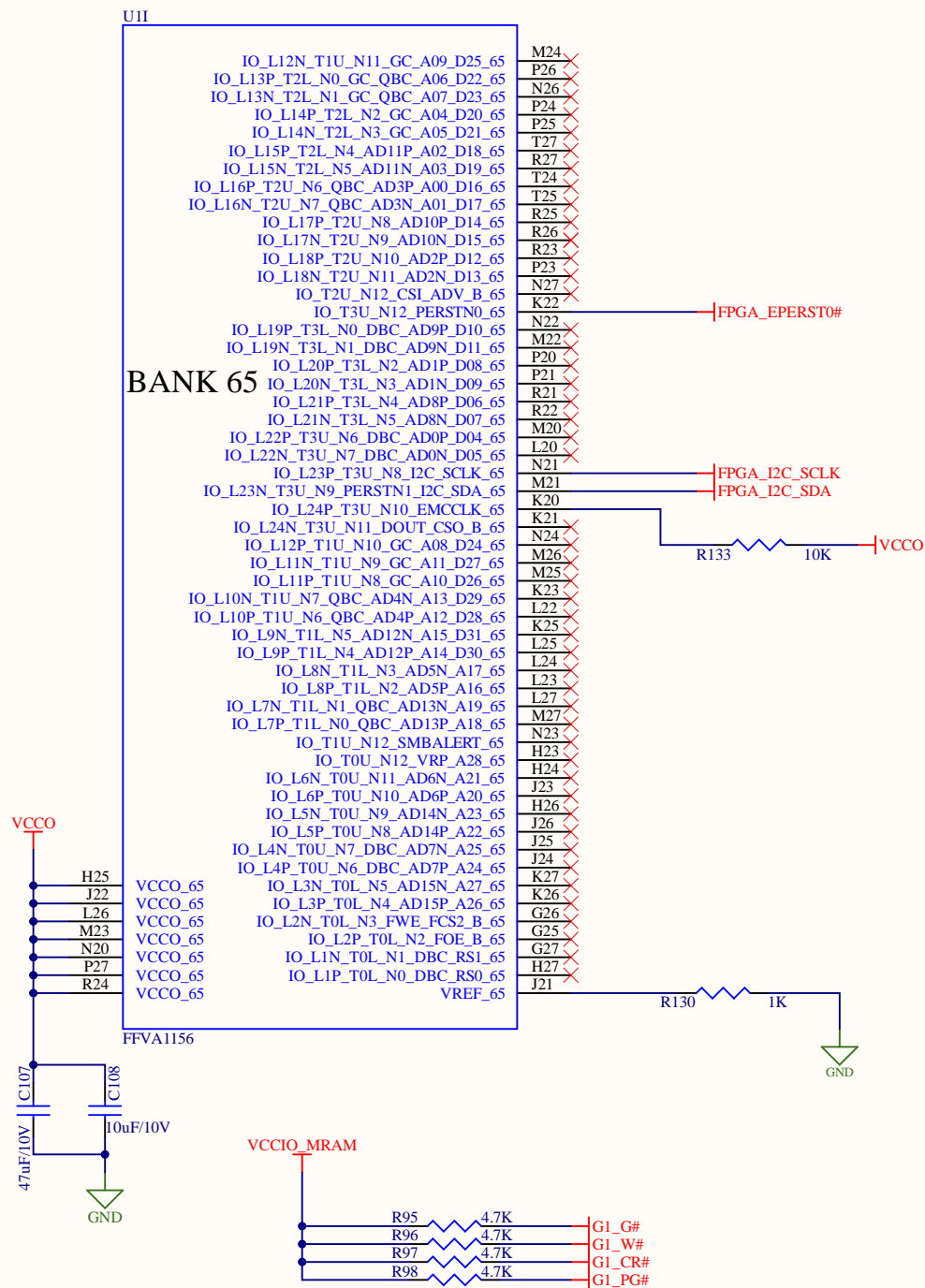
			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 03 OF 28



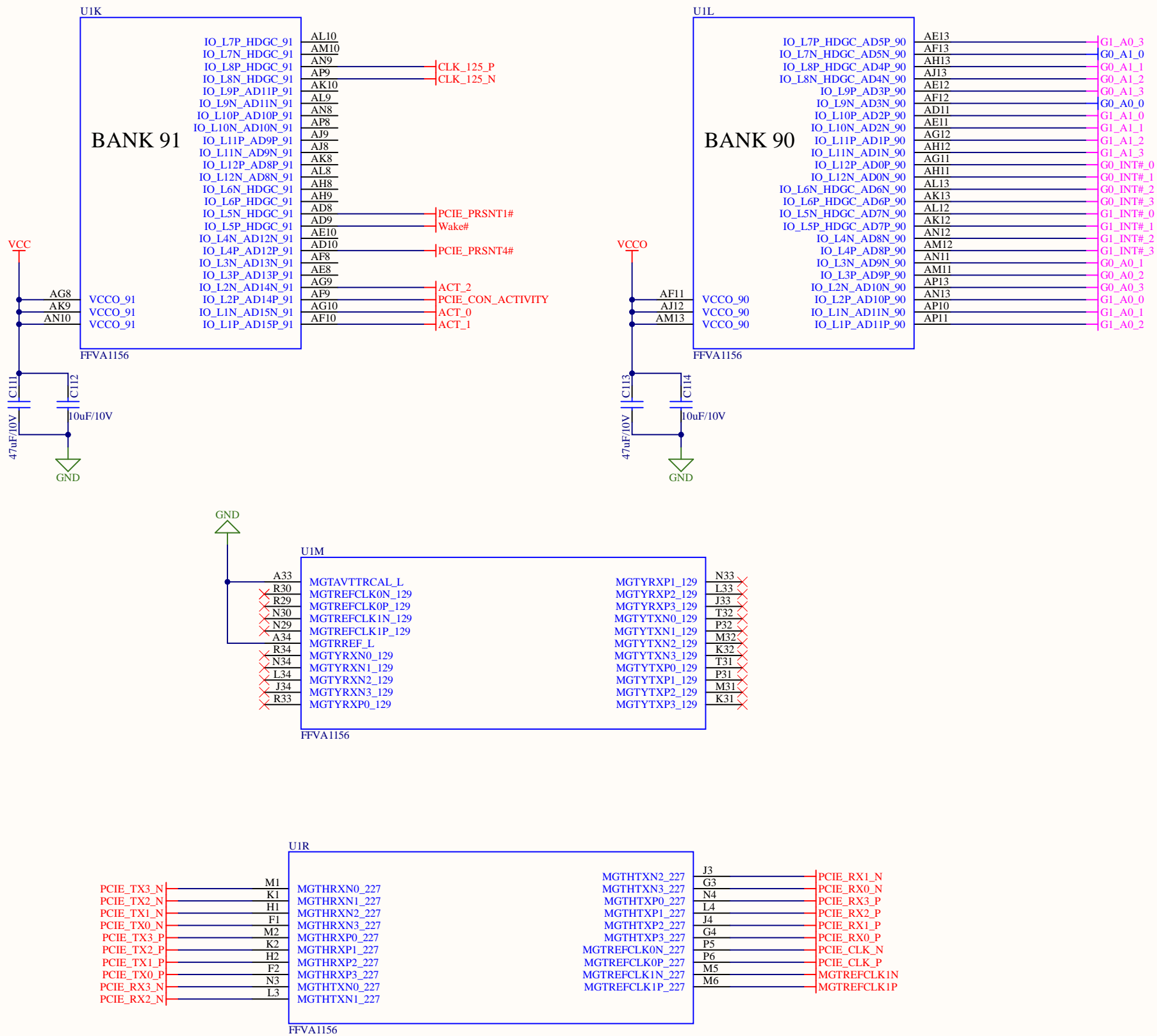
			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:	CUST. CONTACT: XXXX		
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 04 OF 28



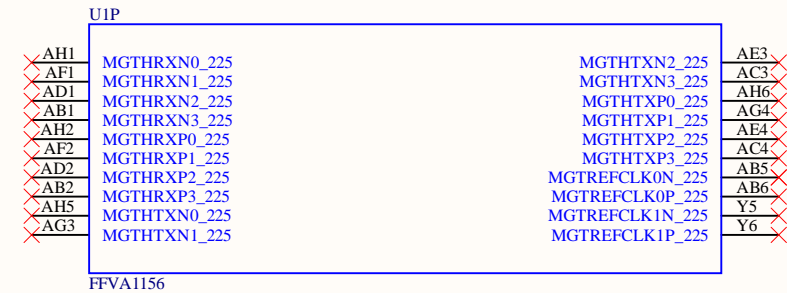
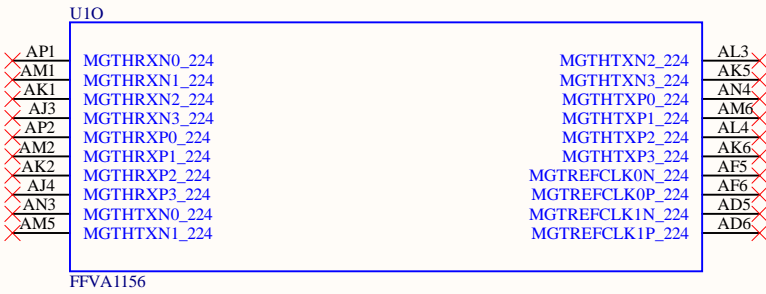
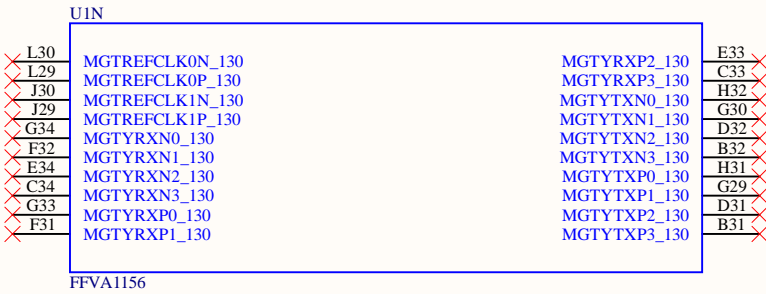
			TITLE: Avalanche PCIE Board				
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX		
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE				
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 05 OF 28	



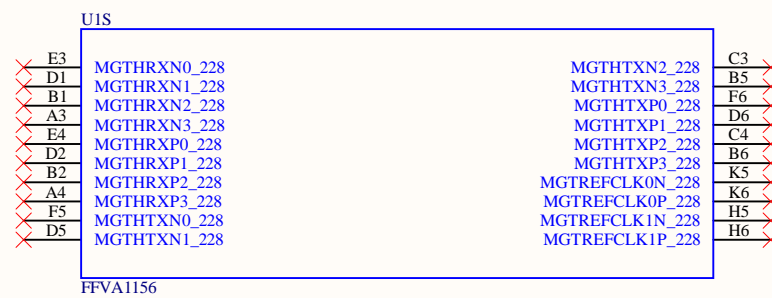
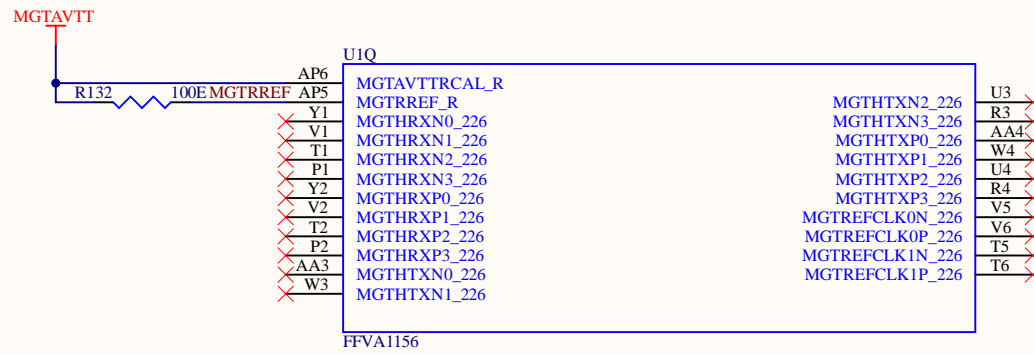
			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 07 OF 28



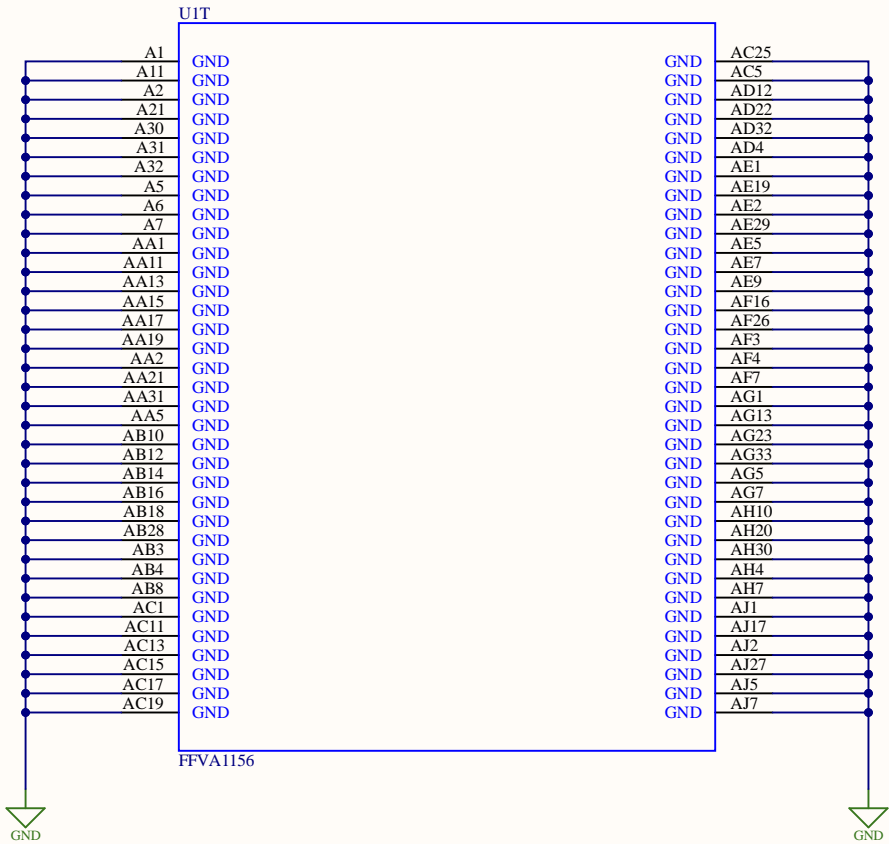
			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 08 OF 28



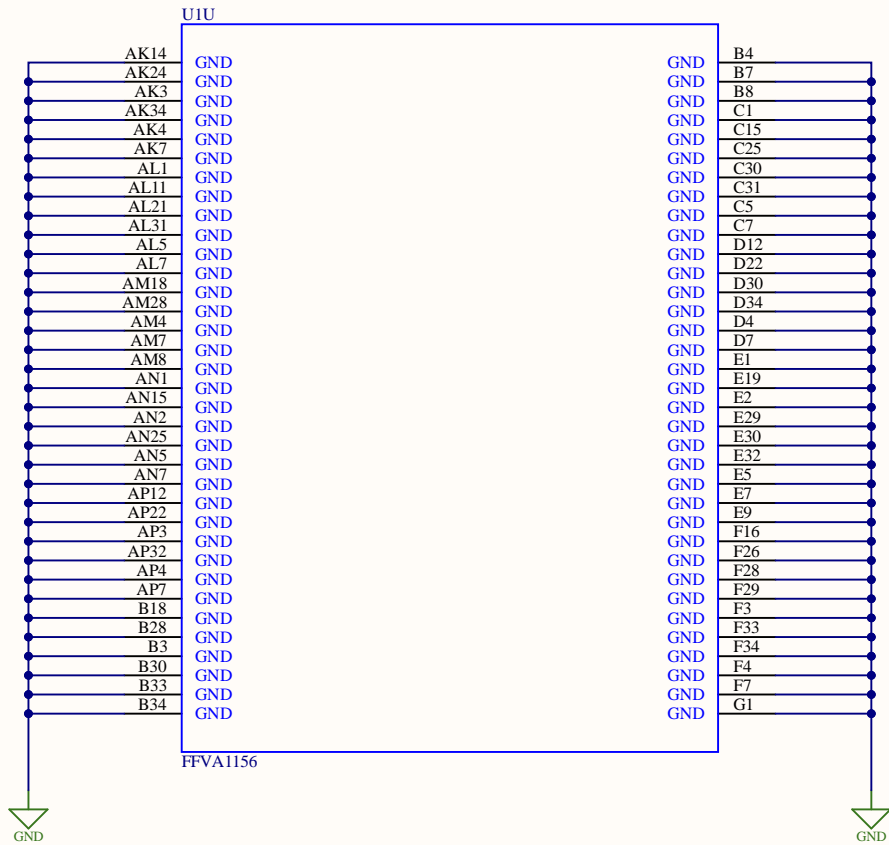
			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:	CUST. CONTACT: XXXX		
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 09 OF 28



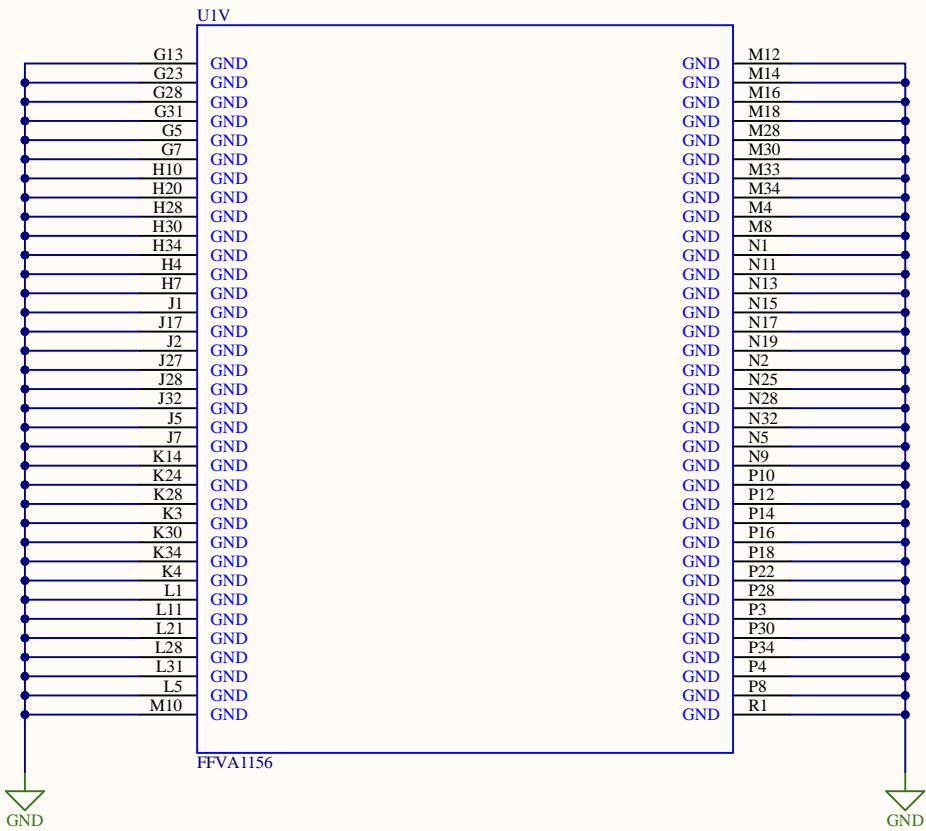
				TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX		
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE				
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 10 OF 28	



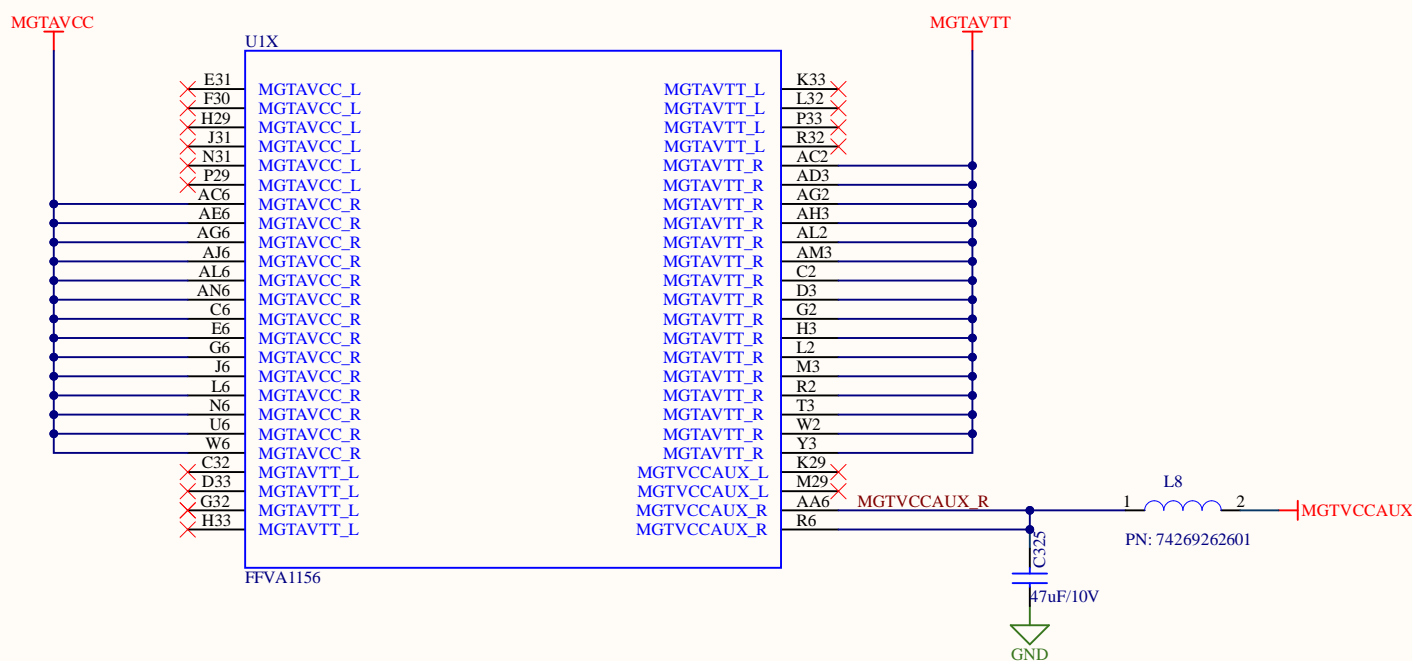
			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:	CUST. CONTACT: XXXX		
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 11 OF 28



			TITLE: Avalanche PCIE Board				
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX		
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE				
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 12 OF 28	



			TITLE: Avalanche PCIE Board				
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX		
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE				
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 13 OF 28	

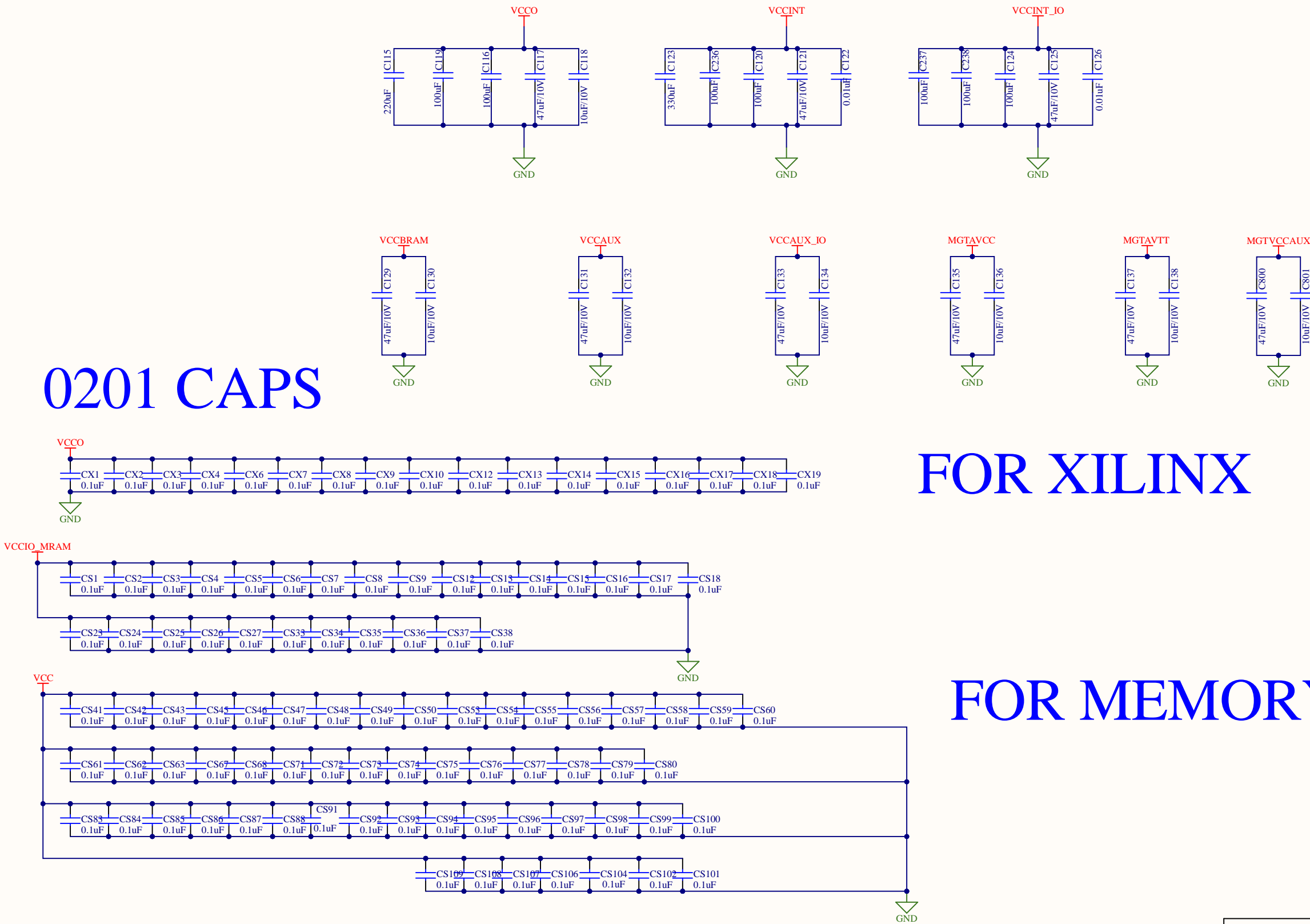


				TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX		
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE				
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 14 OF 28	

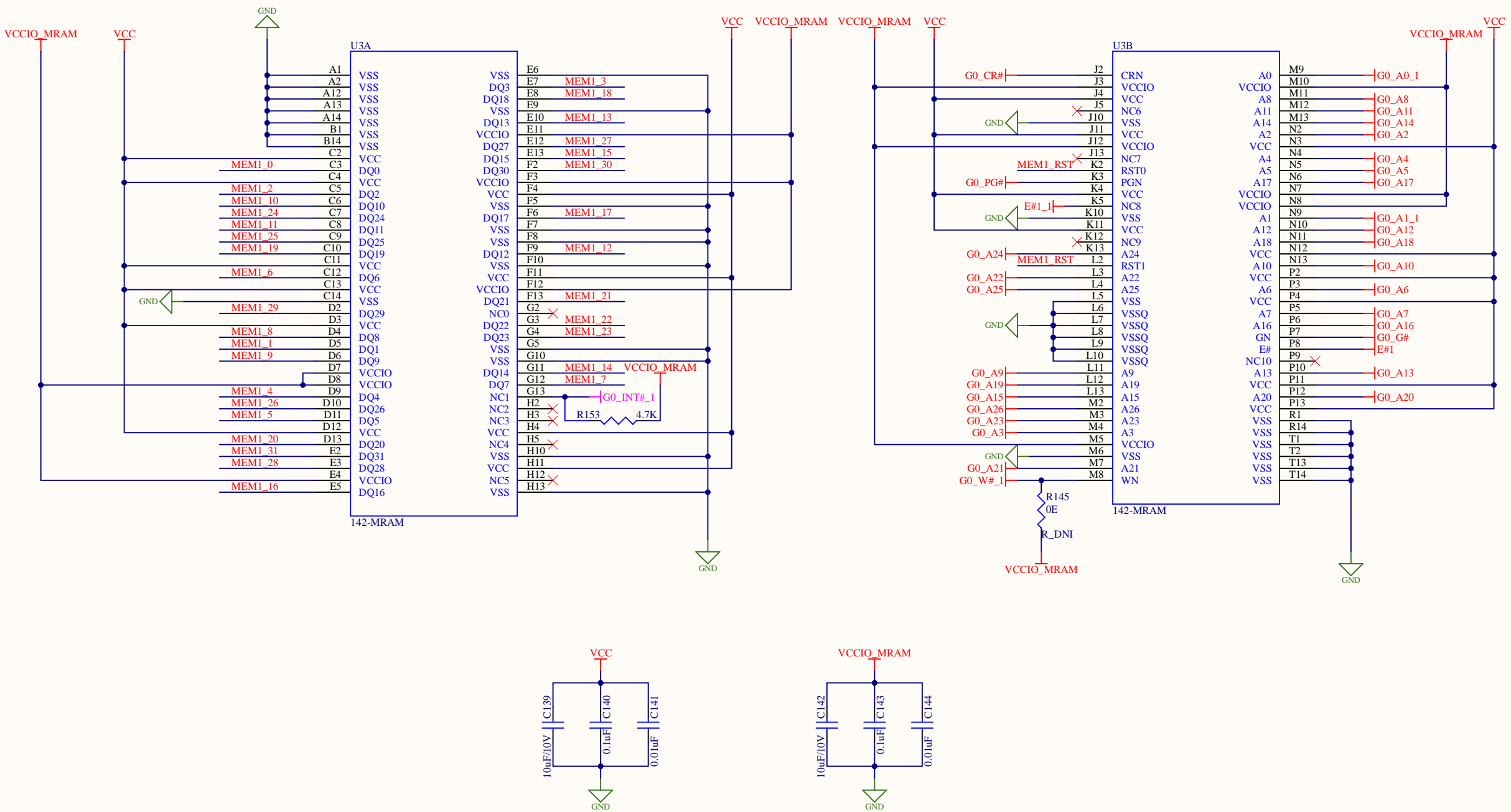
0201 CAPS

FOR XILINX

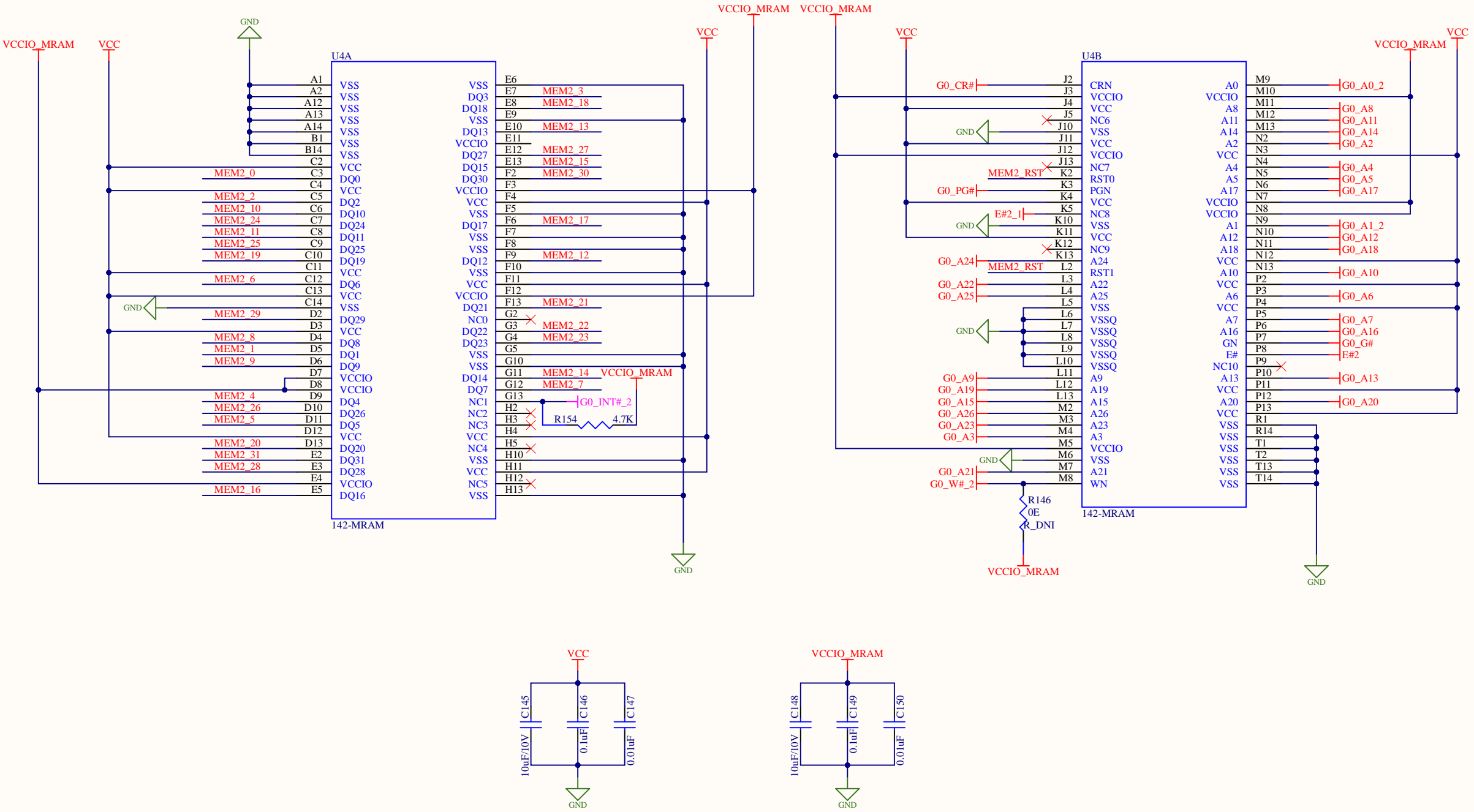
FOR MEMORY



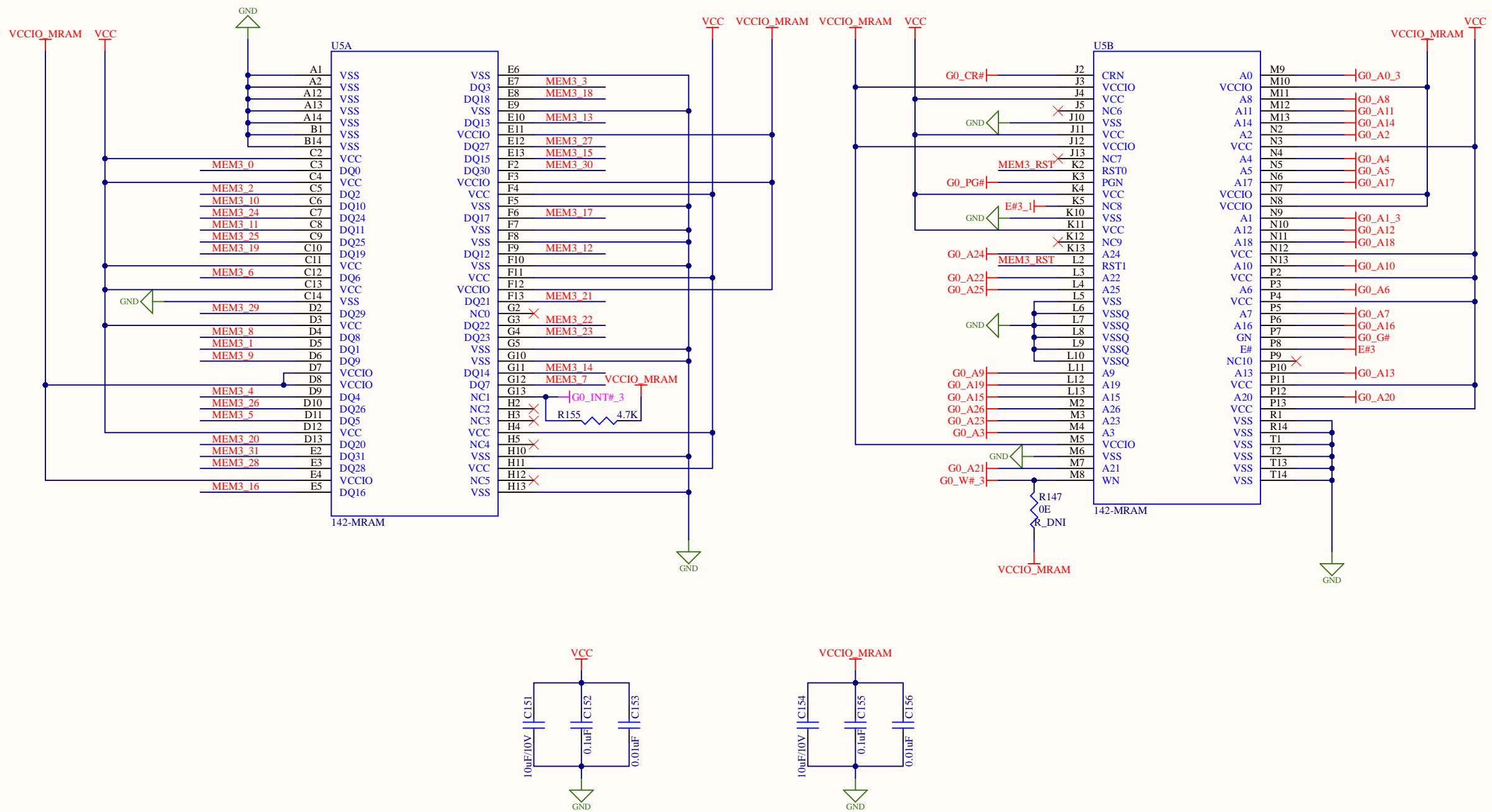
			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 16 OF 28



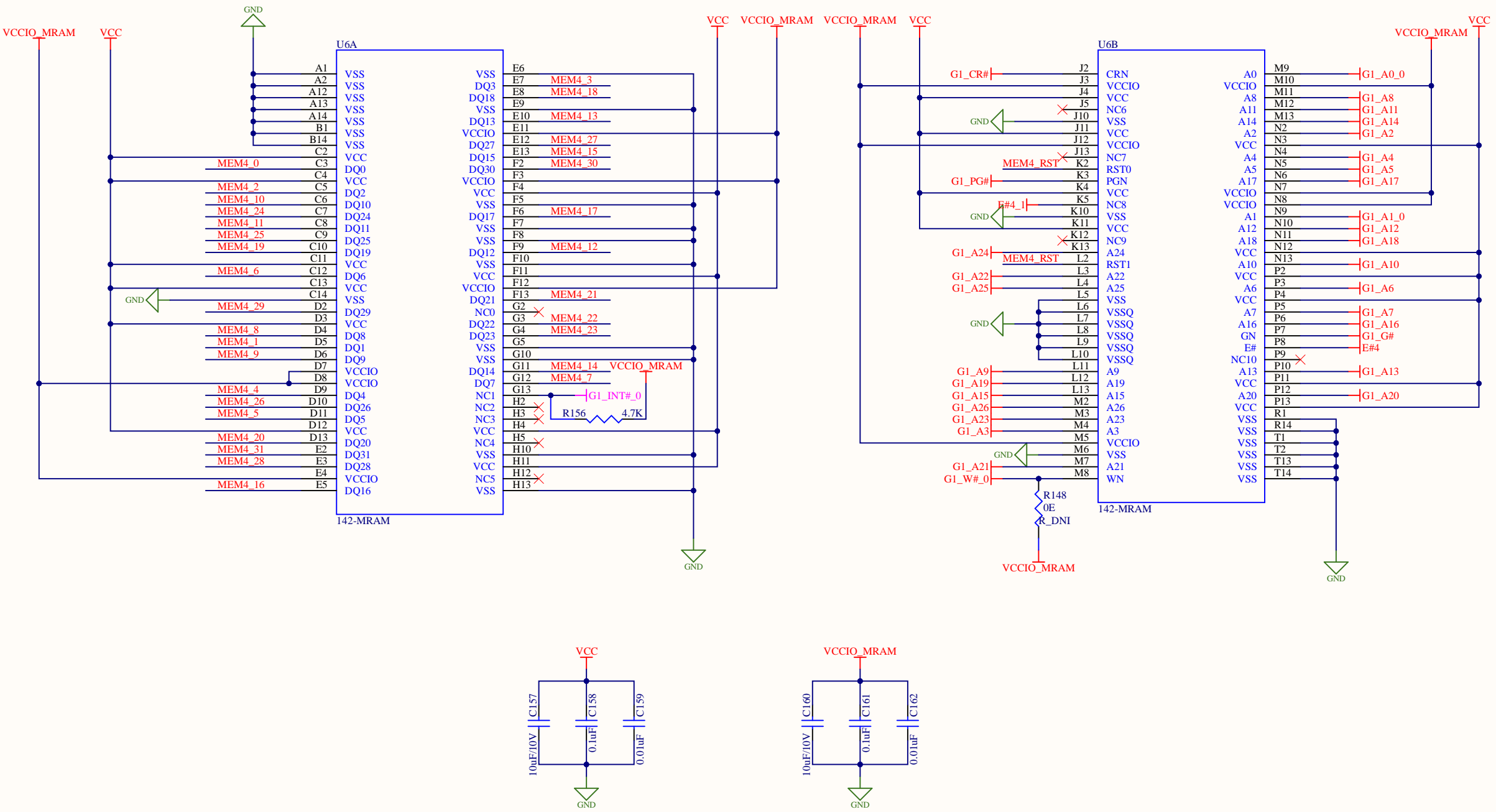
			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 18 OF 28



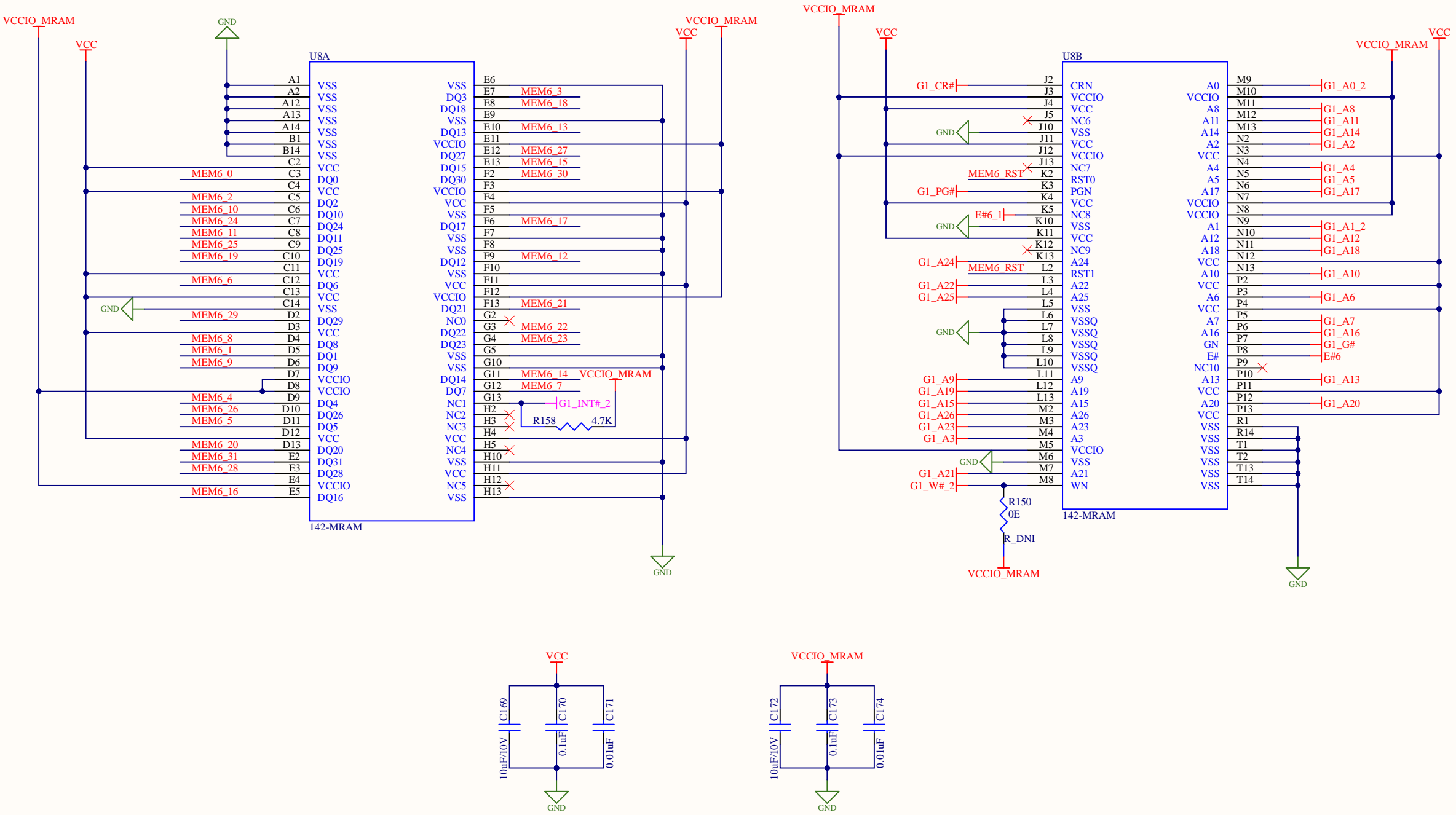
			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 19 OF 28



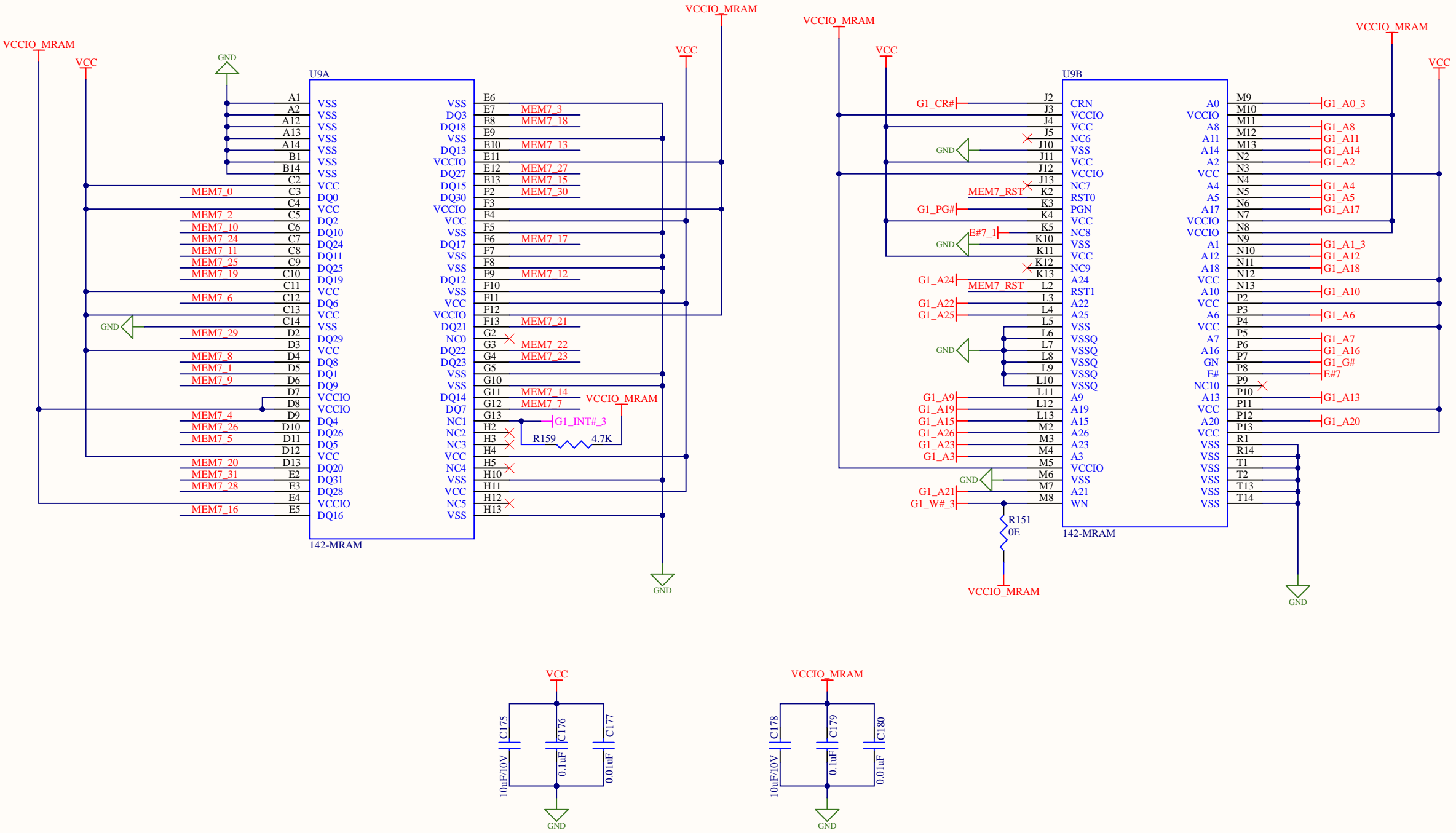
			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 20 OF 28



			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 21 OF 28



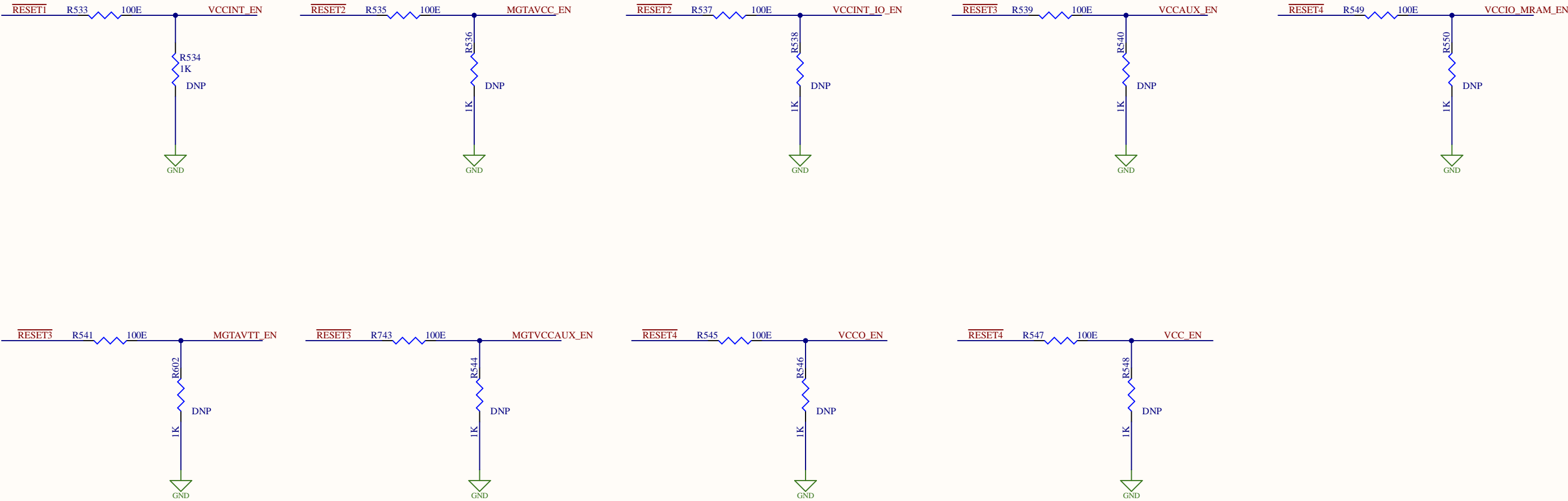
			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 23 OF 28



			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 24 OF 28

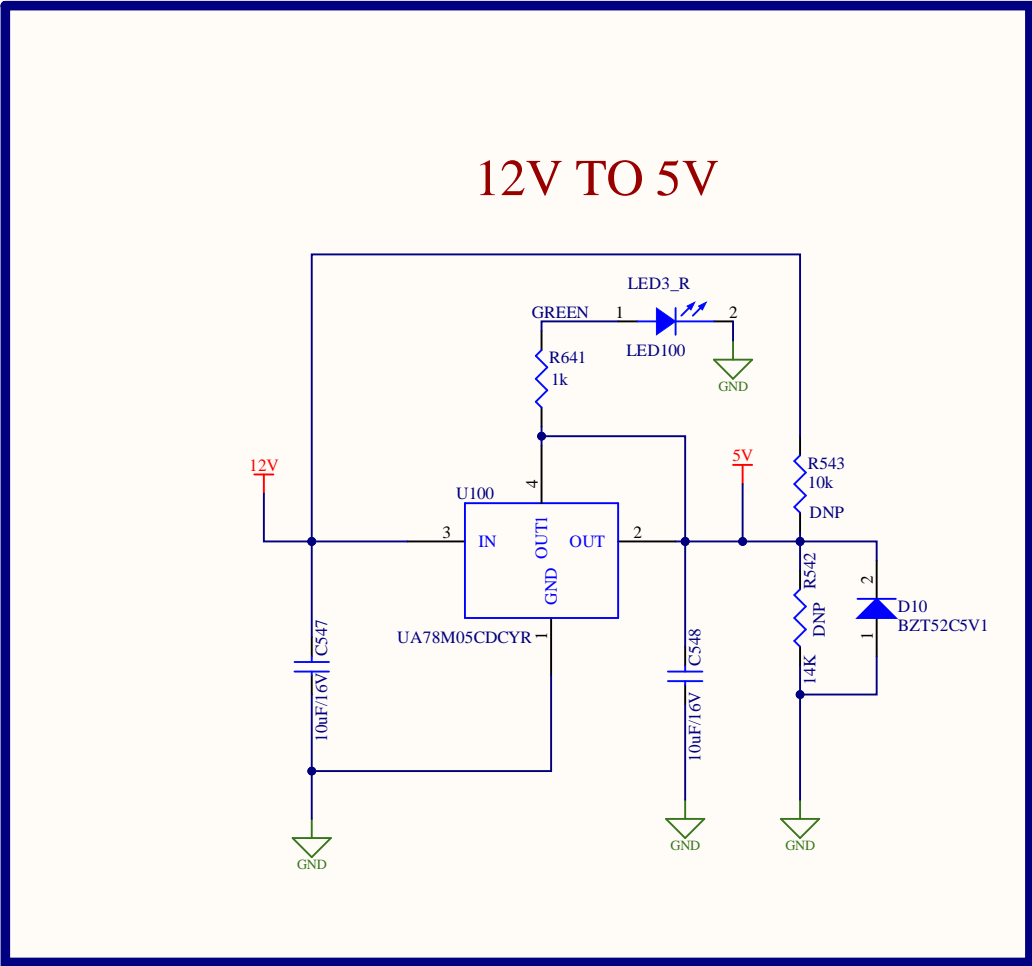
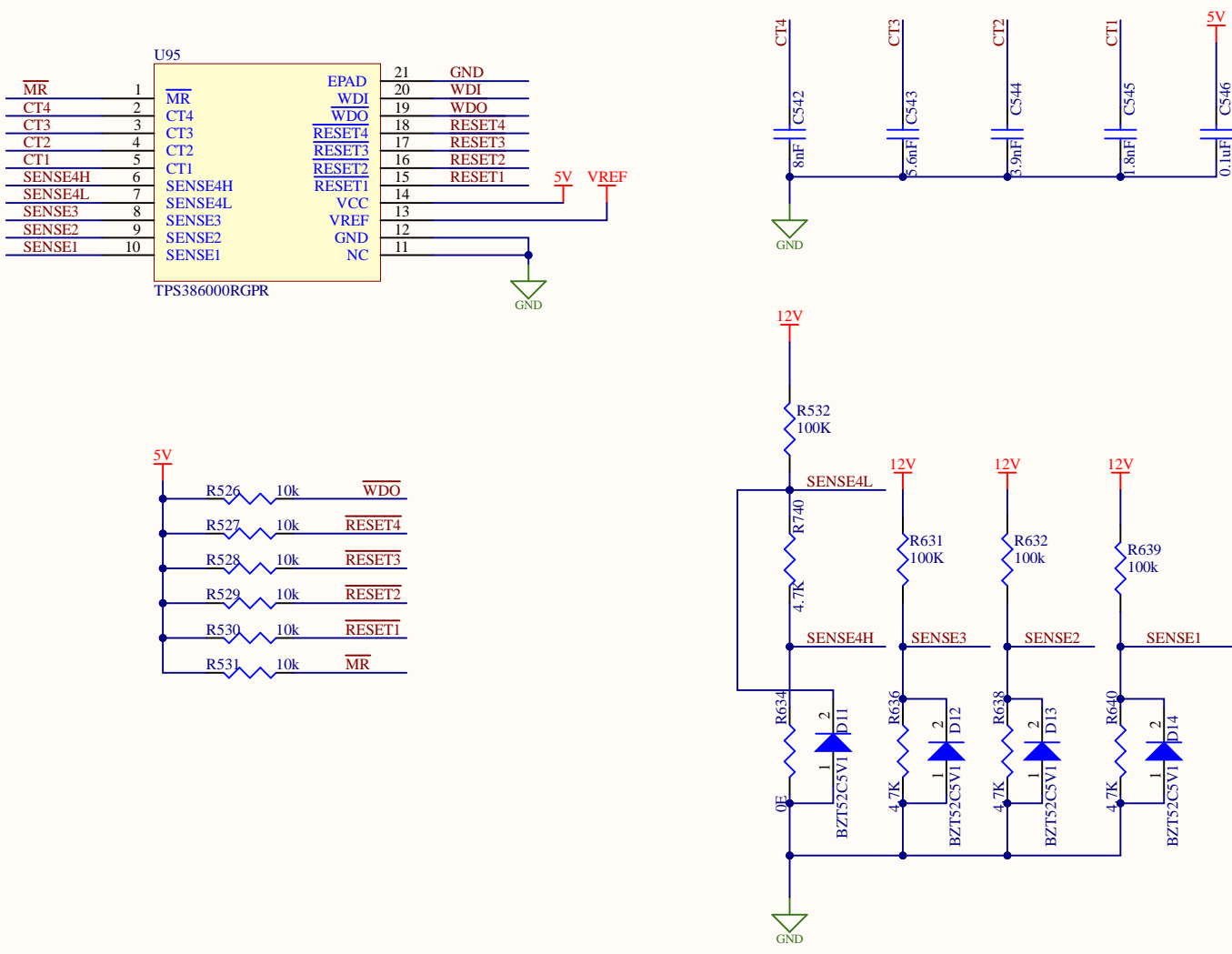
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 25 OF 28

VOLTAGE SUPERVISOR

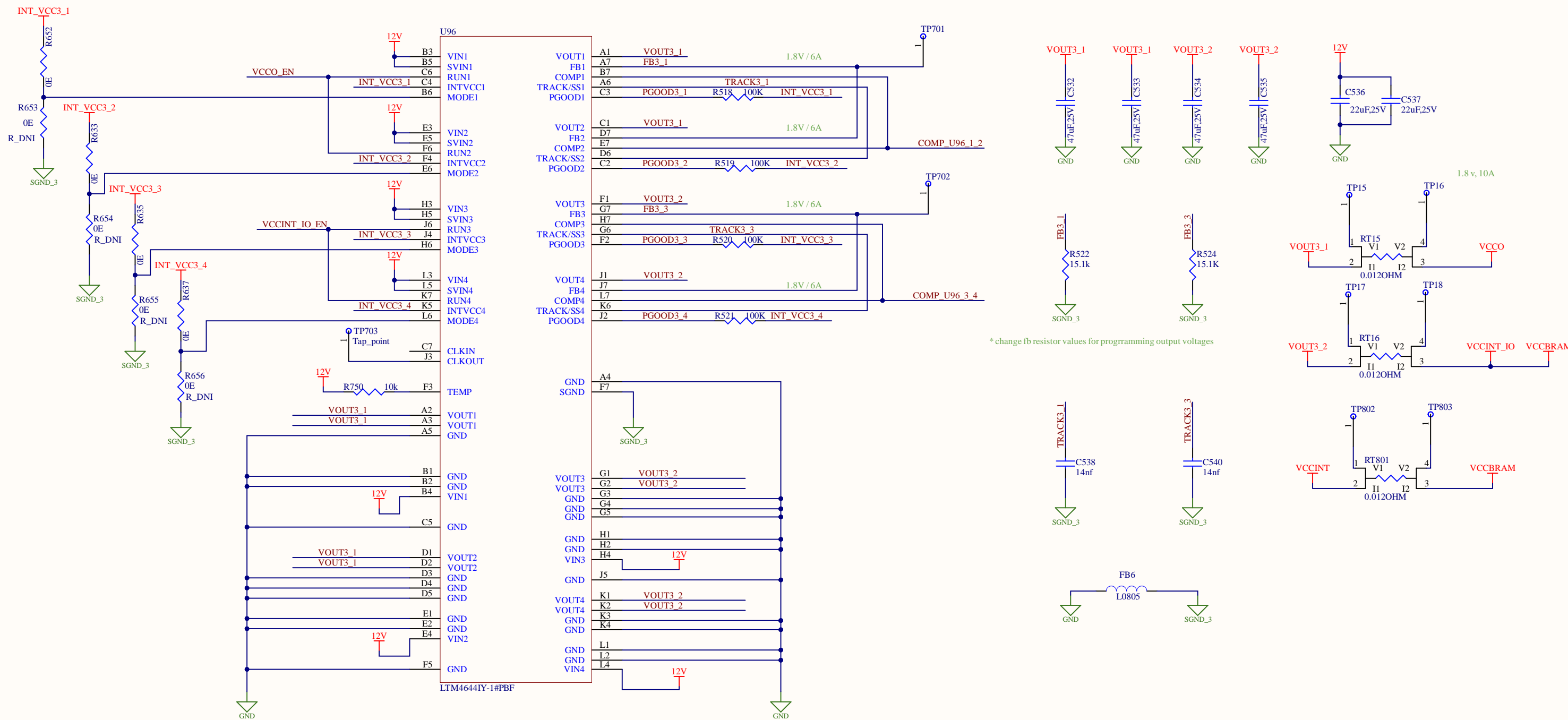


			TITLE: Avalanche PCIE Board			
JOB# QDS11153_AVA	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 27 OF 28

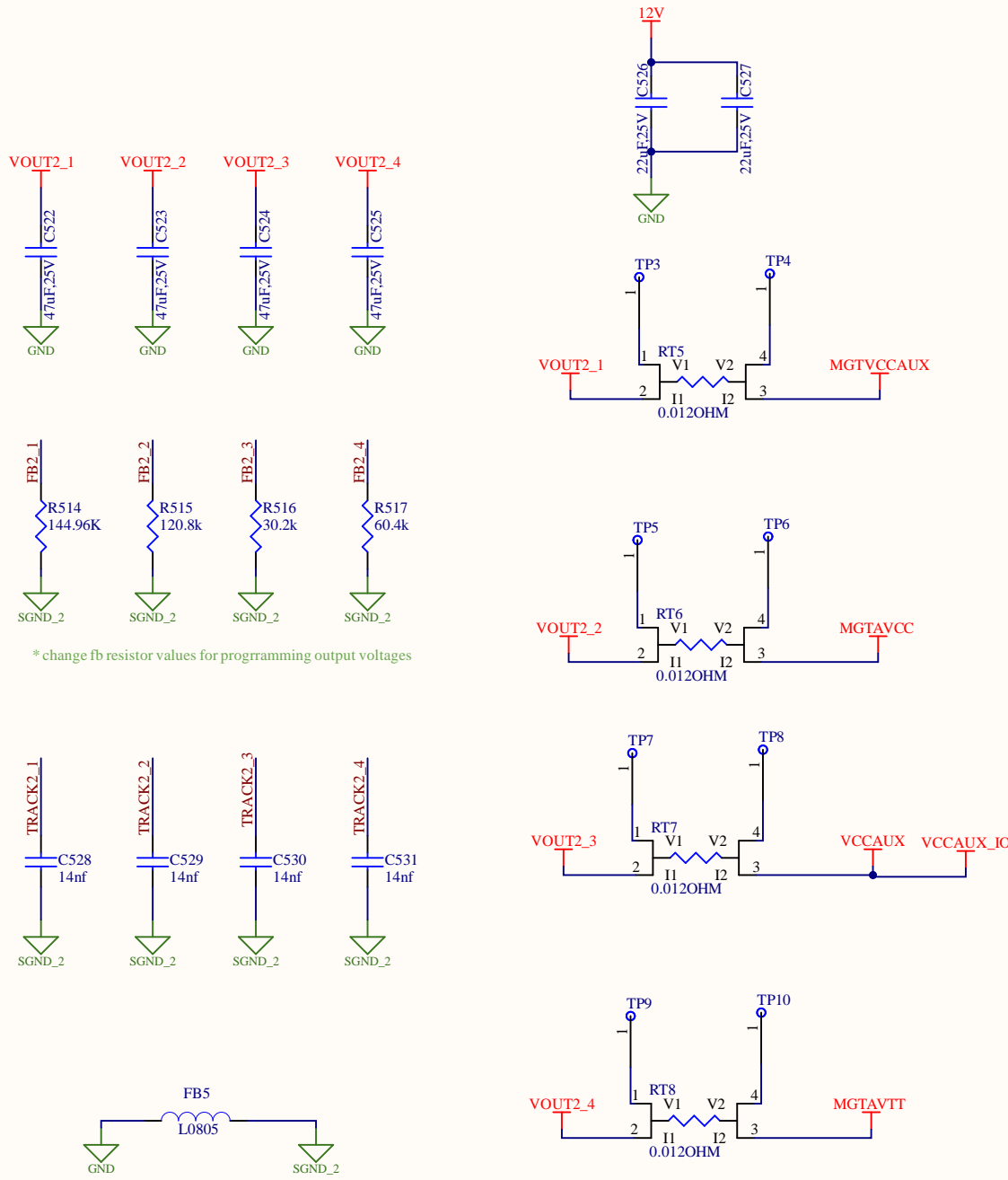
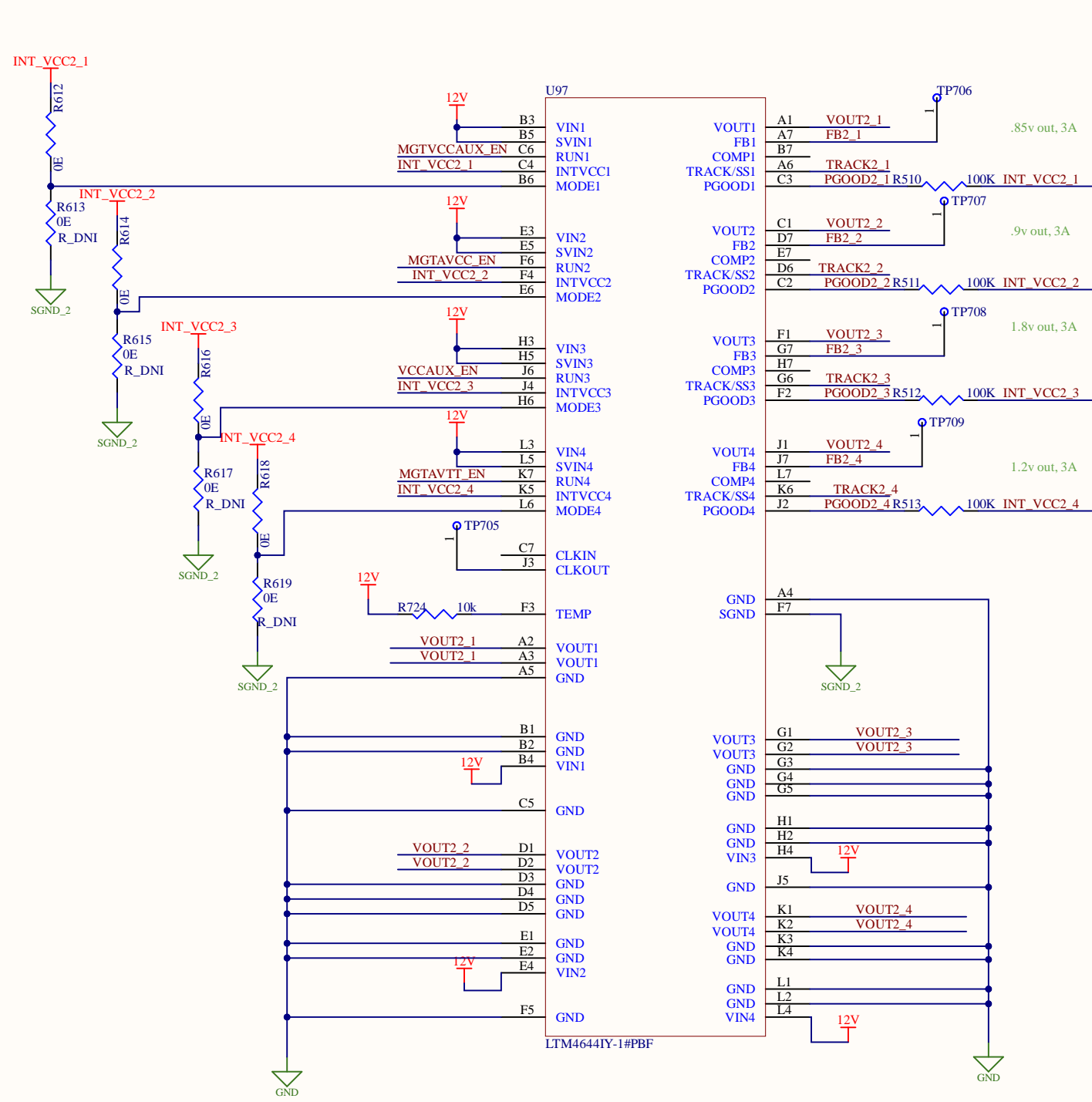
VOLTAGE SUPERVISOR



			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 27 OF 28

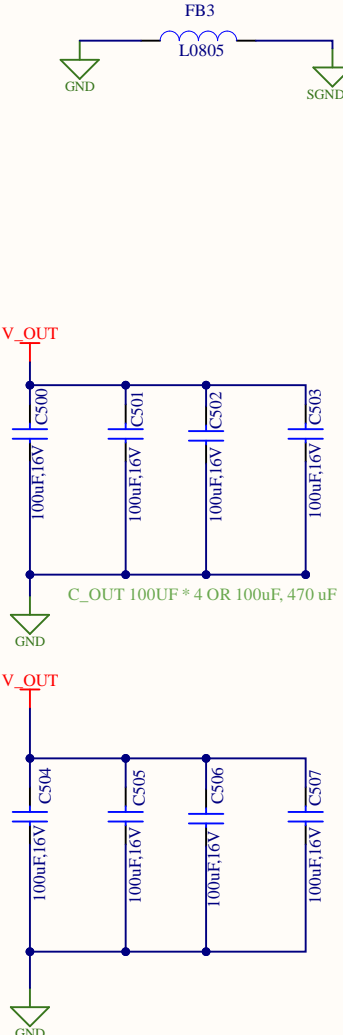
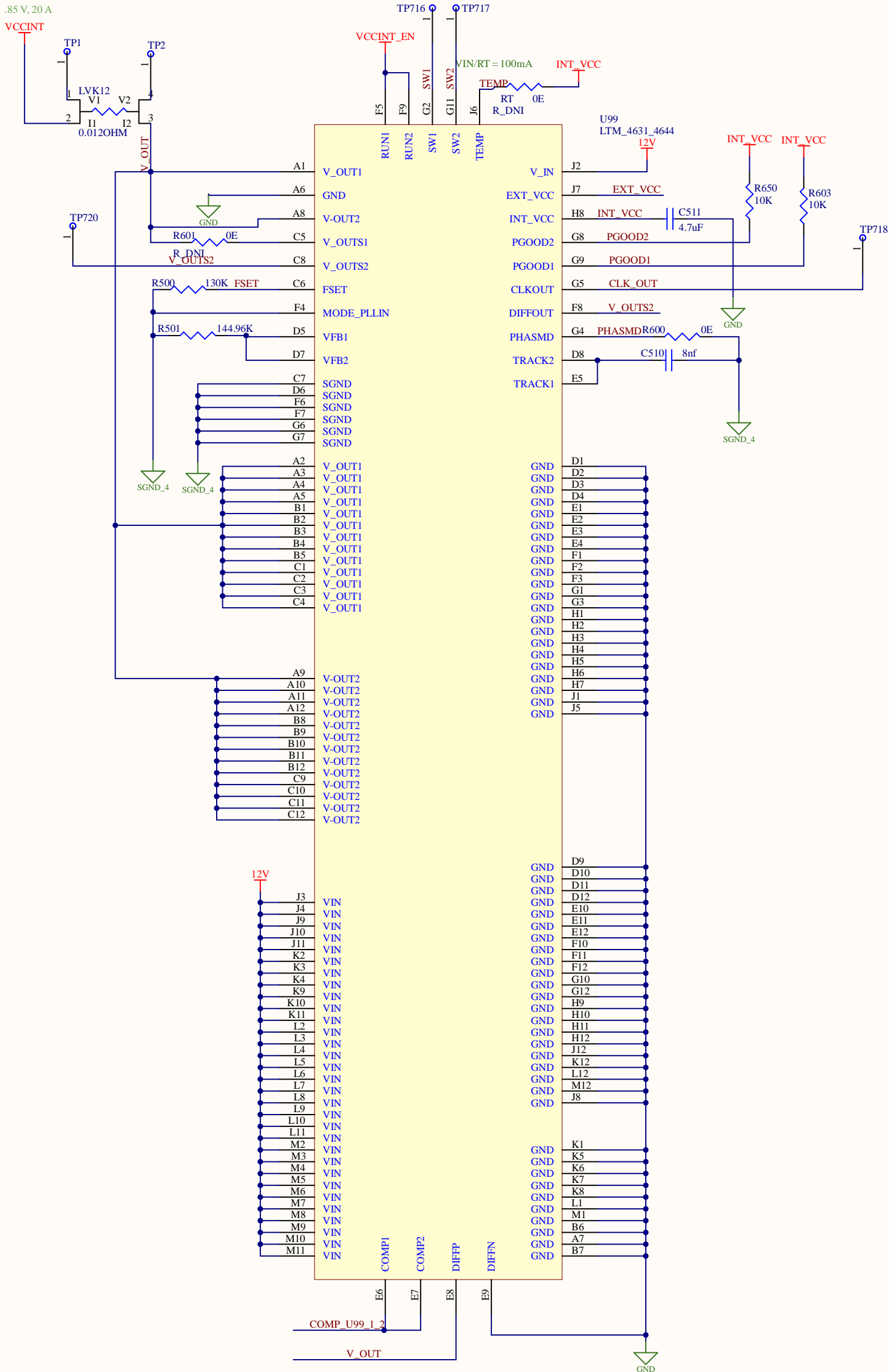


			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 27 OF 28

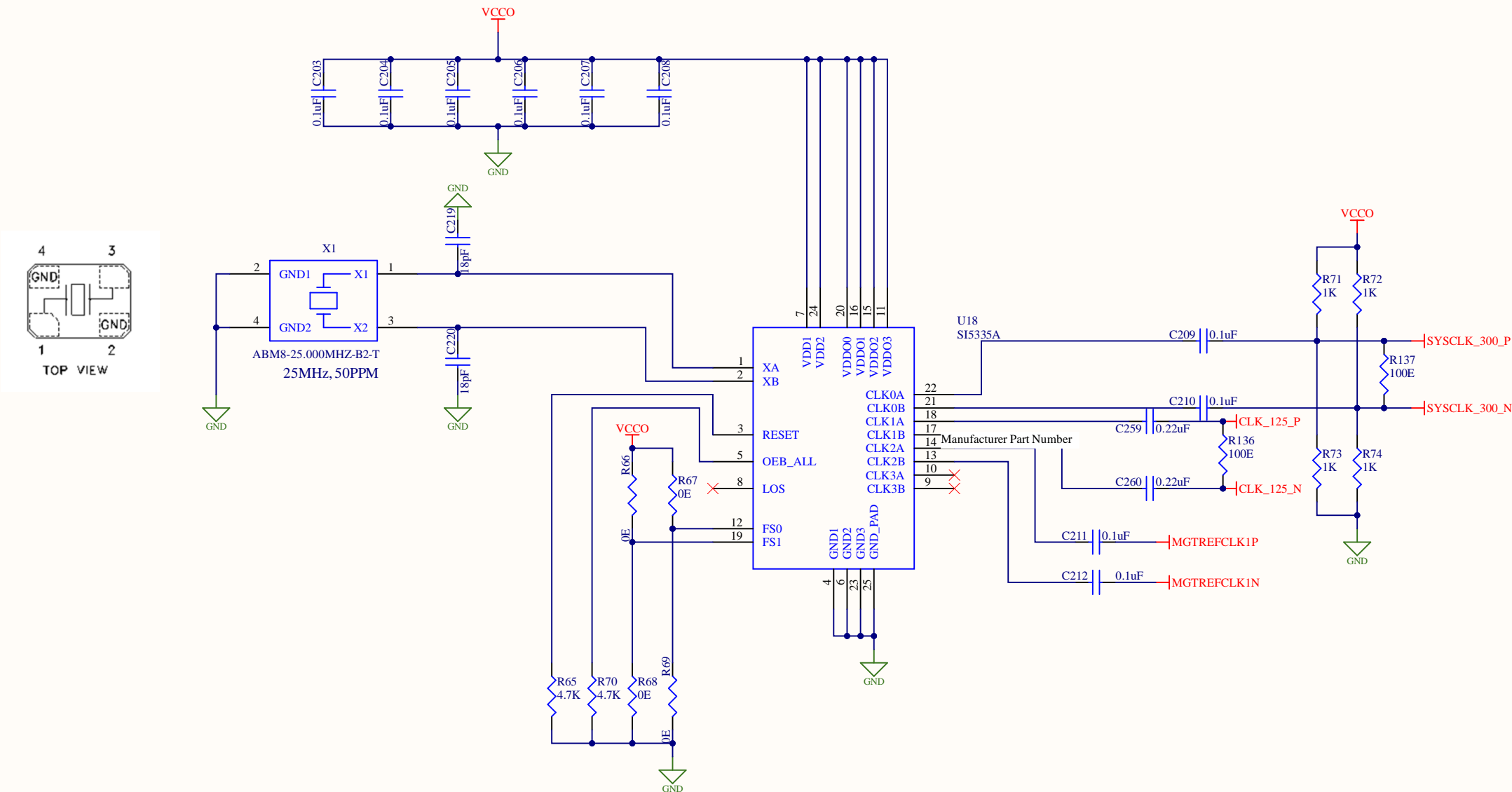


			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 27 OF 28

VCC_INT Power supply



			TITLE: Avalanche PCIE Board				
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX		
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE				
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 27 OF 28	



			TITLE: Avalanche PCIE Board			
	DESIGNED BY: Quoc Ho	DATE: 3/7/2025	CUST. P/N:		CUST. CONTACT: XXXX	
	CHECK BY: XXXX	DATE: 3/7/2025	CUSTOMER: AVALANCHE			
JOB# QDS11153_AVA	APPROVE BY: XXXX	DATE: 3/7/2025	SCALE: NONE	SIZE : B	REV 0.7	SHEET: 28 OF 28